

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor:	Robert M. Zeidman	§	Atty.Dkt.No.:	6257-16302
		§		
Serial Number:	10/044,217	§	Examiner:	Luu, Cuong V.
		§		
Filing Date:	November 19, 2001	§	Group/Art Unit:	2128
		§		
Title:	SYSTEM AND METHOD FOR	§	Conf. No.:	9153
	CONNECTING A LOGIC	§		
	CIRCUIT SIMULATION TO A	§		
	NETWORK	§		
		§		
		§		
		§		
		§		
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		§		

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On: <u>June 24, 2011</u>	<u>/Alex A. Courtade/</u>
Date	Alex A. Courtade, #65,635

**RESPONSE TO OFFICE ACTION MAILED MARCH 24, 2011**

This paper is submitted in response to an Office Action of March 24, 2011, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.